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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Matthew J. Adiletta

Art Unit : 2183 Examiner: Richard L. Ellis

Serial No. :

Filed

Title

10/615,500

July 8, 2003

: PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

TERMINAL DISCLAIMER UNDER 37 C.F.R. §§3.73(b) AND 1.321(b)

Pursuant to 37 C.F.R. § 3.73(b), INTEL CORPORATION, certifies that it is the assignee of the entire right, title, and interest in the above-referenced application by virtue of an assignment from the inventors of the patent application referenced above. The assignment was recorded in the Patent and Trademark Office at Reel 010627 Frame 0746 on February 22, 2000. INTEL CORPORATION also certifies that it is the assignee of U.S Pat. No. 6,606,704.

The undersigned has reviewed all the documents in the chain of title of the abovereferenced application and to the best of undersigned's knowledge and belief, title is in INTEL CORPORATION.

The undersigned is empowered to act on behalf of the assignee.

Pursuant to 37 C.F.R. § 1.321(b), and to obviate a double patenting rejection, the assignee referenced above hereby waives and disclaims the terminal portion of the term of the entire patent to be granted upon the above-referenced application subsequent to the expiration date of, provided that any patent granted on the above-referenced application shall be enforceable only for and during such period that it is commonly owned with U.S Pat. No. 6,606,704.

The assignee referenced above does not disclaim any terminal part of any patent granted on the above-referenced application prior to the expiration date of the full statutory term of U.S

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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